AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

Claim 1 (Currently amended): A memory device comprising:

a first tamper resistant memory which cannot be accessed directly by an external electronic device[[s]]; ~~and~~

a second non-tamper resistant memory which cannot be directly accessed by the external electronic device; ~~and~~

a data processing section that ~~saves~~ moves data in the first tamper resistant memory or the second non-tamper resistant memory[[,]]; and

a managing table in which managing information for the data stored in the first memory is described, the managing information including information indicating whether or not the data can be moved,

wherein when requested via the data processing section by the external electronic device to download or install first data in the first memory and if there is no space area for downloading or installing the first data in the first memory, said data processing section moves to the second memory ~~arbitrary~~ second data which is accumulated in the first memory ~~and possible to be moved~~ in order to create space area in the

first memory sufficient to perform the downloading or installing the first data upon reception of an instruction from the electronic device, and when there is space area available in the first memory, said processing section restores the moved second data in the second memory into the first memory, the second data being possible to be moved and determined on the basis of an instruction from the electronic device.

Claim 2 (Currently amended): The memory device according to claim 1,

wherein the ~~saved~~ second data is data prepared when installing an application program or executing the application program.

Claim 3 (Currently amended): The memory device according to claim 2,

wherein when the second data is ~~saved~~ moved to the second memory, the program code of the application program is rejected from the first memory.

Claim 4 (Currently amended): The memory device according to claim 2,

wherein when the second data is ~~saved~~ moved to the second memory, the program code of the application program is left in the first memory.

Claim 5 (Currently amended): The memory device according to claim 1,

wherein the ~~saved~~ second data includes the data prepared when installing the application program or executing the application program and the program code of the application program.

Claim 6 (Cancelled):

Claim 7 (Original): The memory device according to claim 2,

wherein the application program is downloaded in the first memory and installed in the first memory.

Claim 8 (Original): The memory device according to claim 2,

wherein the application program is downloaded in the second memory and installed in the first memory.

Claim 9 (Original): The memory device according to claim 2,

wherein the application program is downloaded in the second memory and installed in the second memory.

Claim 10 (Currently amended): The memory device according to claim 1,

wherein the ~~saved~~ second data and the signature information for the second data are encoded and ~~saved~~ moved to the second memory.

Claim 11 (Currently amended): The memory device according to claim 1,

wherein the first memory includes a saved information managing unit for managing saved information, the second data ~~to be saved~~ is encoded and ~~saved~~ moved, and the signature information of the encoded second data is stored in the saved information managing unit.

Claims 12-13 (Cancelled):

Claim 14 (Currently amended): The memory device according to claim 1,

wherein ~~specific saved~~ the second data is restored in accordance with a restoration instruction from the electronic device.

Claim 15 (Currently amended): The memory device according to claim 1,

wherein the ~~saved~~ second data related to the application program is restored in accordance with a start instruction of the application program from the electronic device.

Claim 16 (Cancelled):

Claim 17 (Previously presented): The memory device according to claim 1, further comprising an inner CPU which can directly access to both the first memory and the second memory.